

**IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: Instruction Level Multithreading in an Embedded Processor Using  
Zero-Time Context Switching  
EXAMINER: David J. Huisman  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

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Dated: February 12, 2010

By: /Brian G. Brannon/  
Brian G. Brannon, Reg. No. 57,219

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ALEXANDRIA, VA 22313-1450

**AMENDMENT K**

SIR:

Responsive to the Office Action dated August 12, 2009, please enter the following amendments, and consider the accompanying remarks. Applicant respectfully requests reconsideration of the claims in the above-identified application in view of the following amendments and remarks for the purpose of putting the application in condition for allowance.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Drawings begin on Page 4 of this paper.

Amendments to the Claims begin on page 5 of this paper.

Remarks begin on page 15 of this paper.